Application Data Sheet

Application Information

Application Type::	Regular
Subject Matter::	Utility
Suggested Group Art Unit::	N/A
CD-ROM or CD-R?::	None
Sequence submission?::	None
Computer Readable Form (CRF)?::	No
Title::	OFFSET TRIM CIRCUIT AND METHOD FOR A CONSTANT-TRANSCONDUCTANCE RAIL-TO-TAIL CMOS INPUT CIRCUIT
Attorney Docket Number::	08211/0200255-US0
Request for Early Publication?::	No
Request for Non-Publication?::	Yes
Total Drawing Sheets::	2
Small Entity?::	No
Petition included?::	No
Secrecy Order in Parent Appl.?::	No

Applicant Information

Applicant Authority Type::	Inventor
Primary Citizenship Country::	Netherlands
Status::	Full Capacity
Given Name::	Willem
Middle Name::	Johannes
Family Name::	Kindt
City of Residence::	Sunnyvale
State or Province of Residence::	CA

Country of Residence::	US
Street of mailing address::	1063 Morse Avenue 3-203
City of mailing address::	Sunnyvale
State or Province of mailing address::	CA
Postal or Zip Code of mailing address::	94089

Correspondence Information

Correspondence Customer Number::	38845

Representative Information

Representative Customer Number::	38845

Assignee Information

Assignee name::	National Semiconductor
Street of mailing address::	2900 Semiconductor Drive
City of mailing address::	Santa Clara
State or Province of mailing address::	CA
Postal or Zip Code of mailing address::	95051-8090